

COOK

PATENT
AFTER FINAL REJECTION
EXPEDITED PROCEDURE EXAMINING GROUP 2800

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Kunihiro TSUBOSAKI Group Art Unit: 2826

Serial No.: 09/917,854 Examiner: Fazli Erdem

Filed: July 31, 2001

For: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE

SEMICONDUCTOR DEVICE

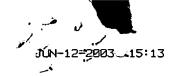
## REQUEST FOR RECONSIDERATION

MAIL STOP AF Commissioner for Patents P. O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Applicant requests reconsideration of the rejections in the Final Rejection mailed February 28, 2003 in view of the following remarks.

The Examiners are thanked for the helpful and courteous interview granted applicant's undersigned attorney on June 5, 2003. At that time, it was agreed that the subject matter of claims 1 to 17 was not taught or suggested by the references cited in the Final

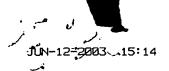


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Rejection because the references did not teach the connection member arrangement of those claims. The claims are directed to a semiconductor device and its method of manufacture wherein the semiconductor chip electrodes and the wiring layer of that device are connected to each other by connection members disposed in an insulation layer and those connection members (1) extend in a direction perpendicular to the semiconductor chip, (2) have a longitudinal shape, and (3) have an area of contact with a wiring layer of the semiconductor device that is less than an area of contact with an electrode. The references, newly cited and applied in the Final Rejection, were agreed at the interview not to show such an arrangement.

Withdrawal of the rejections is earnestly solicited.

Applicant asks the Examiner again to provide his reaction to the Request For Approval of Drawing Change filed November 20, 2002.



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The Examiner is requested to telephone the undersigned if changes are required in the case prior to allowance.

Respectfully submitted,

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